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Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

 (Currently Amended) A validation protocol for determining whether an untrusted authentication chip contained within a consumable is valid, or not, including the steps of:

generating an original random number;

applying, in the a trusted authentication chip contained within a consuming device, an asymmetric encryption function to the random number using a first key from the trusted authentication chip to produce an encrypted random number;

passing the encrypted random number to an-the untrusted authentication chip;

decrypting, in the untrusted authentication chip, the encrypted random number with an asymmetric decryption function using a second secret key from the untrusted authentication chip to produce a decrypted random number;

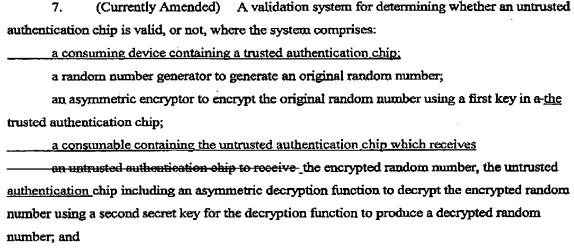
comparing the decrypted random number with the original random number, without knowledge of the second secret key, and in the event of a match considering the untrusted chip to be valid; and,

otherwise considering the untrusted chip to be invalid.

- 2. (Original) A validation protocol according to claim 1, where the random number is not secret, but where the trusted authentication chip contains a random function to produce random numbers from a seed, and the function advances after every random number is produced so that the next random number will be produced from a new seed.
- (Original) A validation protocol according to claim 1, where the first key is a public key.
- 4. (Original) A validation protocol according to claim 1, where the encryption is implemented in software.
- 5. (Original) A validation protocol according to claim 1, where the encryption is implemented in a second authentication chip.

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6. (Original) A validation protocol according to claim 1, where the keys used for encryption and decryption are 2048 bits or larger.



comparison means to compare the decrypted random number with the original random number, without knowledge of the second secret key;

whereby, in the event of a match between the decrypted random number and the original random number, the untrusted chip is considered to be valid; otherwise the untrusted chip is considered to be invalid.

- 8. (Original) A validation system according to claim 7, where the random number generator, encryptor and comparison means are in an external system.
- 9. (Currently Amended) A validation system according to claim <u>87</u>, where the external system is in a device in which it is mounted, and the untrusted chip is in a consumable product consuming device is a printer and the consumable device is an ink cartridge.
- 10. (Original) A validation system according to claim 7, where the random number generator and encryptor are in a second authentication chip, and the comparison means are in an external system which receives the random number and the encrypted version before passing only the encrypted version to the untrusted chip; the system also receives back the decrypted version from the untrusted chip and performs the comparison.

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- 11. (Currently Amended) A validation system according to claim 10, where the system is in a device in which consumables are mounted., and the untrusted chip is in the consumable
- 12. (Original) A validation system according to claim 7, where the random number is not secret, but the random number generator includes a random function to produce random numbers from a seed, and the function advances after every random number is produced so that the next random number will be produced from a new seed.
- 13. (Original) A validation system according to claim 7, where the first key is a public key.
- 14. (Original) A validation system according to claim 7, where the encryption is implemented in software.
- 15. (Original) A validation system according to claim 7, where the encryption is implemented in a second authentication chip.
- 16. (Original) A validation system according to claim 7, where the keys used for encryption and decryption are 2048 bits or larger.